AO 120 (Rev. 08/10)

## TO: Mail Stop 8 Director of the U.S. Patent and Trademark Office P.O. Box 1450 Alexandria, VA 22313-1450

# REPORT ON THE FILING OR DETERMINATION OF AN ACTION REGARDING A PATENT OR TRADEMARK

		15 U.S.C. § 1116 you are hereby advised that a court action has been  Southern District of New York on the following
filed in the U.S. Distr		tion involves 35 U.S.C. § 292.):
DOCKET NO. 07CV825 (AKH)	DATE FILED 2/2/2007	U.S. DISTRICT COURT Southern District of New York
PLAINTIFF		DEFENDANT
ANVIK CORPORATION		SHARP CORPORATION, ET AL.,
PATENT OR TRADEMARK NO.	DATE OF PATENT OR TRADEMARK	HOLDER OF PATENT OR TRADEMARK
See Attached Sheet		See Attached Sheet
24 924 257		
35,285,236		
4 5,291,240		
5		
	L	
	In the above-entitled case, the	he following patent(s)/ trademark(s) have been included:
DATE INCLUDED	INCLUDED BY	nendment Answer Cross Bill Other Pleading
PATENT OR	DATE OF PATENT	incirdinent
TRADEMARK NO.	OR TRADEMARK	HOLDER OF PATENT OR TRADEMARK
See Attached Sheet		See Attached Sheet
2		
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	/e-entitled case, the following	ng decision has been rendered or judgement issued:
DECISION/JUDGEMENT		
COPY ATTACHED: JU	DGMENT	
CLERK	(B)	BY) DEPUTY CLERK DATE
Ruby J. Krajick		4/6/2012
		//

#### UNITED STATES DISTRICT COURT SOUTHERN DISTRICT OF NEW YORK

USDC SDNY
DOCUMENT
BLECTRONICALLY FILED
DOC 8:
DATE FILED: 4/1/2

ANVIK CORPORATION. Plaintiff, NIKON PRECISION, INC., et al., Civ. No. 05-7891 (AKH) Civ. No. 07-0816 (AKH) LG.PHILIPS LCD CO., LTD., et al., SAMSUNG ELECTRONICS AMERICA, INC., et al., : Civ. No. 07-0818 (AKH) CHI MEI OPTOELECTRONICS, et al., : Civ. No. 07-0821 (AKH) AU OPTRONICS CORP., et al., : Civ. No. 07-0822 (AKH) Civ. No. 07-0825 (AKH) SHARP CORP., et al., INNOLUX DISPLAY CORP., Civ. No. 07-0826 (AKH) HANNSTAR DISPLAY CORP., Civ. No. 07-0827 (AKH) AFPD PTE LTD., and Civ. No. 07-0828 (AKH) IPS ALPHA TECHNOLOGY, LTD, et al. Civ. No. 08-4036 (AKH) Defendants.

### JUDGMENT

Upon the Court's Order of April 22012 (the "Order"), it is hereby ordered that the above-captioned actions are dismissed upon the following terms:

 Claims 17 and 18 of U.S. Patent No. 4,924,257 are declared invalid for failure to comply with the requirement that "[t]he specification... shall set forth the best mode contemplated by the inventor of carrying out his invention." 35 U.S.C. § 112.

- Claims 23 and 25 of U.S. Patent 5,285,236 are declared invalid for failure to
  comply with the requirement that "(t)he specification . . . shall set forth the best mode
  contemplated by the inventor of carrying out his invention." 35 U.S.C. § 112.
- Claim 25 of U.S. Patent 5,291,240 is declared invalid for failure to comply with
  the requirement that "[t]he specification . . . shall set forth the best mode contemplated by the
  inventor of carrying out his invention." 35 U.S.C. § 112.
- Plaintiff's Complaint or Amended Complaint, as the case may be, in each of the above-captioned actions is dismissed with prejudice.
  - This is without prejudice to Plaintiff's right to appeal.
- Defendants' Counterclaims, other than those asserting invalidity of U.S. Patents
   Nos. 4,924,257, 5,285,236, and 5,291,240 in each of the above-captioned actions, are dismissed without prejudice as moot.

April 5 ,2012

CLERK OF COURT

Office States Latent (15)				
Jain				
[54]	SCAN ANI	D REPEAT HIGH RESOLUTION ION LITHOGRAPHY SYSTEM		
[76]	Inventor:	Kantilal Jain, 18 Algonquian Trail, Briarcliff Manor, N.Y. 10510		
[21]	Appl. No.:	253,717		
[22]	Filed:	Oct. 5, 1988		
[51] [52]	Int. Cl. <sup>5</sup> U.S. Cl			
[58]	Field of Ser 356/400	arch		
[56]		References Cited		
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	4,701,608 10/ 4,748,477 5/ 4,749,867 6/ 4,825,086 4/	1988 Isohata et al		

[57] ABSTRACT
This scan and repeat lithography system has high resolution capability, large effective image field size, and

high substrate exposure speed, and comprises: (a) a

Primary Examiner-L. T. Hix

Assistant Examiner-D. Rutledge

Attorney, Agent, or Firm-Carl Kling

United States Patent no

substrate stage capable of scanning a substrate in one dimension and, when not scanning in said dimension, capable of moving laterally in a direction perpendicular to the scan direction so as to position the substrate for another scan; the substrate stage exposing the full substrate by breaking up the substrate area into parallel strips, and exposing each of the strips by scanning the length of the strip across a fixed illumination region; (b) a mask stage capable of scanning in the same direction as, and synchronized with, the substrate stage, at a speed faster than the substrate stage scanning speed by a certain ratio M; (c) an illumination subsystem having an effective source plane in the shape of a polygon, and capable of uniformly illuminating a polygon-shaped region on the mask; (d) a projection subsystem having an object-to-image reduction ratio M, and having a polygon-shaped image field of an area smaller than the desired effective image field size of the lithography system: and (e) provision of complementary exposures in an overlap region between the areas exposed by adjacent scans in such a way that a seam in the exposure dose distribution received on the substrate is absent between the scans, and such that the exposure dose delivered across the entire substance is uniform.

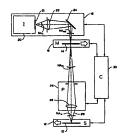
Patent Number:

Date of Patent:

4,924,257

May 8, 1990

22 Claims, 5 Drawing Sheets



US005285236A [11] Patent Number: Date of Patent:

5,285,236 Feb. 8, 1994

## United States Patent [19] Jain

[54]	HIGH-RE	REA, HIGH-THROUGHPUT, SOLUTION PROJECTION SYSTEM
[76]	Inventor:	Kanti Jain, 18 Algonquian Trail, Briarcliff Manor, N.Y. 10510
[21]	Appl. No.	954,662
[22]	Filed:	Sep. 30, 1992
[52] [58]	U.S. Cl	G03B 27/53 355/53; 355/67 arch 355/53, 66, 67
[56]		References Cited
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	5,168,306 12/ 5,227,838 7/	1990     Hirose     355/53       1990     Jain     355/53       1992     Morimoto et al.     355/53       1993     Nakanishi et al.     355/53       1993     Allen     355/53
Prim	ary Examin	r-Michael L. Gellner

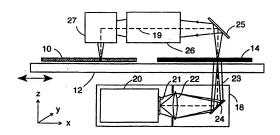
Assistant Examiner-D. P. Malley

ABSTRACT

This projection imaging system has large-area exposure capability, high exposure throughput, and high resolu-

tion, and comprises: (a) a stage for holding in fixed juxtaposition a mask and a substrate, and capable of scanning in one dimension, and when not scanning in that dimension, capable of moving laterally in a direction perpendicular to the scan direction so as to position itself for another scan, the stage exposing the full substrate by breaking up the substrate area into parallel strips, and exposing each strip by scanning the length of the strip across a fixed illumination region; (b) an illumination system having an effective source plane of a predetermined shape, and capable of illuminating on the mask a region of the above predetermined shape; (c) a projection assembly having an object-to-image magnification ratio of unity, having means to render the image in the same orientation as the object, and having an image field of the above predetermined shape and of an area smaller than the substrate area; and (d) provision for additive illumination in overlap regions of areas exposed by adjacent scans such that the effect of the exposure dose delivered in the overlap regions is seamless and the effect of the exposure dose delivered across the entire substrate is uniform.

28 Claims, 5 Drawing Sheets



### United States Patent [19]

#### US005291240A [11] Patent Number:

5,291,240

[45] Date of Patent:

Mar. 1, 1994

#### [54] NONLINEARITY-COMPENSATED LARGE-AREA PATTERNING SYSTEM

[75] Inventor: Kanti Jala, Briarcliff Manor, N.Y. [73] Assignee: Anvik Corporation, Elmsford, N.Y.

[21] Appl. No.: 967,189

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Jain

Oct. 27, 1992 [22] Filed:

. G03B 27/42 Int. Cl.<sup>5</sup> ...... .. 355/53; 355/77 [58] Field of Search ...... 355/50, 51, 53, 77; 250/492.2, 548

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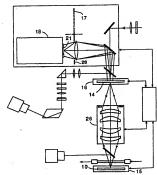
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Primary Examiner-Richard A. Wintercorn Attorney, Agent, or Firm-Carl C. Kling

ABSTRACT

This patterning system has the ability to uniformly image a mask onto a substrate having nonlinear exposure characteristics, has large-area exposure capability, and comprises: (a) a stage system capable of synchronously scanning a mask and a substrate in one dimension, and when not scanning in that dimension, capable of moving them laterally in a direction perpendicular to or moving them increasy in a direction perpendicular to the scan direction so as to position the mask and sub-strate for another scan partially overlapping the preced-ing scan, thus exposing the full substrate in an overlap-ping scan-and-repeat fashion; (b) an illumination system capable of illuminating on the mask a region of a predetermined multisided shape which has at least one of its sides curved, the curvatures of said curved sides being so determined that adjacent scanning exposures are compensated in their overlap regions for the nonlinear response characteristics of the substrate so as to provide uniform cumulative response; (c) a projection assembly capable of imaging the illuminated region on the mask onto the substrate, having the desired imaging resolu-tion, and having an image field size smaller than the substrate; and (d) provision for adjusting the widths of the overlap regions of adjacent scans in such a way that the cumulative response over the entire substrate is uniform.

#### 26 Claims, 6 Drawing Sheets



US005721606A
[11] Patent Number:
[45] Date of Patent:

5,721,606 Feb. 24, 1998

United States Patent [19]

[54] LARGE-AREA, HIGH-THROUGHPUT, HIGH-RESOLUTION, SCAN-AND-REPEAT, PROJECTION PATTERNING SYSTEM

EMPLOYING SUB-FULL MASK

[76] Inventor: Kanti Jain. 18 Algonquian Trail.
Briarcliff Manor, N.Y. 10510

[21] Appl. No.: 524,766 [22] Filed: Sep. 7, 1995

[56] References Cited

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 2/1994
 Jain
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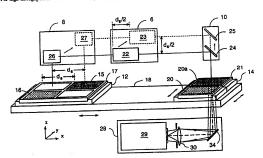
Primary Examiner-John H. Pendegrass Astorney, Agent, or Firm-Carl C. Kling

[57] ABSTRACT

For high-throughput, low-cost manufacturing of electronic

modules, it is desirable to use a large-format, 1:1-imaging exposure system. In such a system, it is further desirable to have the capability to pattern a large substrate having multiple segments using a mask of the size of one substrate segment. The substrate is mounted on an x-y stage which moves the substrate with respect to the imaging optics, both within a segment and from segment to segment. For each mask position, moving from one substrate segment to another will result in a significant change in the length or orientation of the optical imaging path. Such problems are eliminated by using, in conjunction with the primary x-y stage, an auxiliary stage which maintains the optical parameters essentially constant for the imaging of different substrate segments. The suxiliary stage in a first embodiment is mounted on the primary x-y stage and is deployed to move the mask to compensate for the primary stage motion required to present the subsequent substrate segment at the imaging location and keep the optical parameters constant. In a second embodiment, the auxiliary stage is configured as an optics stage set mounted orthogonally to the x-y stage and moves components of the projection system to present a different substrate segment at the imaging location while keeping the optical imaging parameters constant. A third embodiment employs auxiliary stages for moving both the mask and the projection optics for greater versatility.

#### 22 Claims, 5 Drawing Sheets



## TISOS807986A

[11] Patent Number:

5,897,986

[45] Date of Patent:

Apr. 27, 1999

#### [54] PROJECTION PATTERNING OF LARGE SUBSTRATES USING LIMITED-TRAVEL X-Y STAGE

United States Patent 1191

- [75] Inventors: Thomas J. Dunn, Mohegan Lake, N.Y.; Nestor O. Farmiga, Clifton, N.J.; Kenti Jain, Briarcliff Manor, N.Y.
- [73] Assignce: Anvik Corporation. Hawthorne, N.Y.
- [21] Appl. No.: 08/864,160

Dunn et al.

h

- [22] Filed: May 28, 1997

#### [56] References Cited

### U.S. PATENT DOCUMENTS

Primary Examiner-John A. McPherson Attorney, Agent, or Firm-Carl C. Kling

#### [57] ABSTRACT

A large-format substrate patterning system, for microelectronics manufacturing, utilizes a substrate docking fixture to enable relative motion between the substrate stage and the substrate. This enables exposure of a large-format substrate which has been partitioned into different modules where water has occup partitioned into different modules. Where each module contains an entire pattern transferred from a mask. This projection system enables patterning of a large multi-module substrate using a stage whose range of travel is smaller than the size of the substrate and using a mask whose area is smaller than the size of the substrate. This is accomplished by repositioning the substrate to expose each module sequentially. In order to reposition the substrate, its location is maintained fixed in space by a substrate docking fixture while the movable stage of the lithography system is repositioned to position a different module of the substrate in the image field of the lithography tool. This allows the use of a mask whose size is determined by the size of each substrate module, and the use of a scanning stage whose required range of travel is determined by the size of the substrate module, and not by the size of the entire substrate. This eliminates the size limitation of the substrate from dependence on the range of travel of the stage, and permits the repetitive use of a small mask or series of small masks to produce a composite multi-module pattern on the sub-

#### 7 Claims, 12 Drawing Sheets

